

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: K. TASAKI, et al.  
Application No.: 10/581,721  
Nat'l Stage Entry: JUNE 5, 2006  
For: MANUFACTURING METHOD FOR ELECTRONIC DEVICE  
Group AU: 2814  
Examiner: Tifney L. Skyles  
Confirm. No.: 8570

**RESPONSE**

**Mail Stop: AMEND – NO FEE**

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

July 8, 2011

Sir:

In response to the Office Action mailed June 9, 2011, Applicants respectfully elect the Group I claims (that is, claims 1-10 and 13-22, characterized by the Examiner as being drawn to a method of making a semiconductor device), for prosecution on the merits in the above-identified application. This election is made without traverse.

Examination of the Group I claims (claims 1-10 and 13-22), on the merits, in the above-identified application, in due course, is respectfully requested.

To the extent necessary, Applicants hereby petition for an extension of time under 37 CFR 1.136. Kindly charge any shortage of fees due in connection with the filing of this paper, including any extension of time fees, to the Deposit Account of